

VPT5-2800S Series



HIGH RELIABILITY COTS DC-DC CONVERTERS

DESCRIPTION

The VPT5 series of isolated COTS DC-DC converters is a cost effective solution for many demanding high reliability applications. A wide input voltage range accommodates nominal 28V inputs including avionics, mobile, ground systems, and other applications. Low input and output ripple, fixed operating frequency, and companion EMI filters simplify system design and compliance. A proven design heritage, no optoisolators and a rugged all metal package ensure long term reliability.

The VPT5 series is intended for harsh environments including severe vibration, shock and temperature cycling. Testing is to JESD22, MIL-STD-810, and MIL-STD-883.

These converters are designed and manufactured in the USA in a facility certified to ISO9001, J-STD-001 and IPC-A-610.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6,118,673

FEATURES

- High Reliability at Low Cost
- 5 Watts Output Power
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704 and MIL-STD-1275
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Input Undervoltage Lockout
- Fixed Frequency
- Output Soft Start
- Current Limit Protection
- Short Circuit Protection
- Magnetic Feedback, no Optoisolators
- Wide Temperature Range, -55°C to 100°C
- Fully Encapsulated
- Six Sided Metal Rugged Enclosure
- Meets MIL-STD-461C/D/E Conducted Emissions Requirements When Used With a VPTF series EMI Filter



Figure 1 – VPT5-2800S DC-DC Converter (Not To Scale)



+10°C

-55°C to +125°C



SPECIFICATIONS (T_{CASE} = -55°C to +100°C, V_{IN} = +28V ± 5%, Full Load, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS Input Voltage (Continuous) $50 V_{DC}$ Junction Temperature Rise to Case Input Voltage (Transient, 1 second) 80 Volts Storage Temperature

Output Power¹ 5 Watts Lead Solder Temperature (10 seconds)

300°C Power Dissipation (Full Load, $T_{CASE} = +100^{\circ}C$) 2.5 Watts Weight (Maximum) 22 Grams

| Parameter | Conditions | V | VPT5-283R3S | | | VPT5-2805S | | |
|---|---|------|-------------|------|------|------------|---|------------|
| Farameter | Conditions | Min | Тур | Max | Min | Тур | Max | Units |
| STATIC | | | | | | | | |
| INPUT , | Continuous | 15 | 28 | 50 | 15 | 28 | 50 | ٧ |
| Voltage ⁴ | Transient, 1 sec | - | - | 80 | - | - | 80 | V |
| Current | Inhibited | - | 4 | 6 | - | 4 | 6 | mA |
| Current | No Load | - | 40 | 60 | - | 40 | 60 | mA |
| Ripple Current | Full Load, 20Hz to 10MHz | - | 30 | 50 | - | 30 | 50 | mA_{p-p} |
| Inhibit Pin Input⁴ | | 0 | - | 1.5 | 0 | - | 1.5 | V |
| Inhibit Pin Open Circuit Voltage ⁴ | | 9.0 | 11.0 | 13.0 | 9.0 | 11.0 | 13.0 | V |
| UVLO Turn On | | 12.0 | - | 14.8 | 12.0 | - | 14.8 | V |
| UVLO Turn Off⁴ | | 11.0 | - | 14.5 | 11.0 | - | 14.5 | V |
| OUTPUT V _{ou} | T _{CASE} = 25°C | 3.25 | 3.30 | 3.35 | 4.92 | 5.00 | 5.08 | V |
| Voltage V _{ou} | $T_{CASE} = -55^{\circ}C \text{ to } +100^{\circ}C$ | 3.21 | 3.30 | 3.38 | 4.87 | 5.00 | 5.13 | V |
| Power ³ | | 0 | - | 5 | 0 | - | 5 | W |
| Current ³ I _{ou} | г | 0 | - | 1.52 | 0 | - | 1.0 | Α |
| Ripple Voltage V _{ou} | Full Load, 20Hz to 10MHz | - | 45 | 60 | - | 25 | 40 | mV_{p-p} |
| Line Regulation V _{ou} | V _{IN} = 15V to 50V | - | 1 | 10 | - | 1 | 10 | mV |
| Load Regulation V _{ou} | No Load to Full Load | - | 1 | 10 | - | 1 | 10 | mV |
| EFFICIENCY | | 62 | 66 | - | 70 | 74 | - | % |
| LOAD FAULT POWER DISSIPATION | Overload ⁴ | - | - | 3.7 | - | - | 3.3 | W |
| LOAD FACEL FOWER DIGGILATION | Short Circuit | - | - | 3 | - | - | 3 | W |
| CAPACITIVE LOAD⁴ | | - | - | 1000 | - | - | 1000 | μF |
| SWITCHING FREQUENCY | | 425 | 500 | 550 | 425 | 500 | 550 | kHz |
| ISOLATION | 500 V _{DC} | 100 | - | - | 100 | - | - | МΩ |
| MTBF (MIL-HDBK-217F) | GM @ T _C = 55°C | - | 468 | - | - | 468 | - | kHrs |
| DYNAMIC | | | | | | | | |
| Load Step Output Transient Vou | Half Load to Full Load | - | 150 | 250 | - | 100 | 300 | mV_{PK} |
| Load Step Recovery ² | Tiali Luau tu Fuli Luau | - | 400 | 600 | - | 300 | 500 | μSec |
| Line Step Output Transient ⁴ V _{OU} | 1 \/ = 16\/ to 40\/ | - | 200 | 400 | - | 200 | 500 | mV_{PK} |
| Line Step Recovery ^{2, 4} | V _{IN} = 16V to 40V | - | 400 | 600 | - | 400 | 80 6 60 1.5 13.0 14.8 14.5 5.08 5.13 5 1.0 40 10 10 - 3.3 3 1000 550 - k 300 π 500 μ 500 π 600 μ 20 π | μSec |
| Turn On Delay V _{ou} | 1 | - | 10 | 20 | - | 10 | 20 | mSec |
| Turn On Overshoot | $V_{IN} = 0V \text{ to } 28V$ | - | 0 | 15 | - | 0 | 25 | mV_{PK} |

Notes:

- 2. Time for output voltage to settle within 1% of its nominal value.
- Dependant on output voltage.
 Derate linearly to 0 at 110°C.
- 4. Verified by qualification testing.





SPECIFICATIONS (T_{CASE} = -55°C to +100°C, V_{IN} = +28V ± 5%, Full Load, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS Input Voltage (Continuous) $50 V_{DC}$ Junction Temperature Rise to Case +10°C Input Voltage (Transient, 1 second) -55°C to +125°C 80 Volts Storage Temperature Output Power¹ 5 Watts Lead Solder Temperature (10 seconds) 300°C Power Dissipation (Full Load, $T_{CASE} = +100^{\circ}C$) 2.5 Watts Weight (Maximum) 22 Grams

| Parameter | Conditions | VPT5-2812S | | | VPT5-2815S | | | Units |
|---|---|------------|------|-------|------------|------|-------|------------|
| Faranietei | Conditions | Min | Тур | Max | Min | Тур | Max | Units |
| STATIC | | | | | | | | |
| INPUT | Continuous | 15 | 28 | 50 | 15 | 28 | 50 | V |
| Voltage ⁴ | Transient, 1 sec | - | - | 80 | - | - | 80 | V |
| Current | Inhibited | - | 4 | 6 | - | 4 | 6 | mA |
| Guirent | No Load | - | 40 | 60 | - | 40 | 60 | mA |
| Ripple Current | Full Load, 20Hz to 10MHz | - | 30 | 50 | - | 30 | 50 | mA_{p-p} |
| Inhibit Pin Input⁴ | | 0 | - | 1.5 | 0 | - | 1.5 | V |
| Inhibit Pin Open Circuit Voltage⁴ | | 9.0 | 11.0 | 13.0 | 9.0 | 11.0 | 13.0 | V |
| UVLO Turn On | | 12.0 | - | 14.8 | 12.0 | - | 14.8 | V |
| UVLO Turn Off⁴ | | 11.0 | - | 14.5 | 11.0 | - | 14.5 | V |
| OUTPUT V _{OUT} | T _{CASE} = 25°C | 11.82 | 12.0 | 12.18 | 14.77 | 15.0 | 15.23 | V |
| Voltage V _{out} | $T_{CASE} = -55^{\circ}C \text{ to } +100^{\circ}C$ | 11.70 | 12.0 | 12.30 | 14.62 | 15.0 | 15.38 | V |
| Power ³ | | 0 | - | 5 | 0 | - | 5 | W |
| Current ³ I _{OUT} | | 0 | - | 0.42 | 0 | - | 0.33 | Α |
| Ripple Voltage V _{OUT} | Full Load, 20Hz to 10MHz | - | 15 | 30 | - | 15 | 30 | mV_{p-p} |
| Line Regulation V _{OUT} | V _{IN} = 15V to 50V | - | 1 | 10 | - | 1 | 10 | mV |
| Load Regulation V _{OUT} | No Load to Full Load | - | 1 | 10 | - | 1 | 10 | mV |
| EFFICIENCY | | 71 | 75 | - | 71 | 76 | - | % |
| LOAD FAULT POWER DISSIPATION | Overload ⁴ | - | - | 3 | - | - | 3 | W |
| EGAD FAGEL FOWER DISSILATION | Short Circuit | - | - | 3 | - | - | 3 | W |
| CAPACITIVE LOAD⁴ | | - | - | 500 | - | - | 500 | μF |
| SWITCHING FREQUENCY | | 425 | 500 | 550 | 425 | 500 | 550 | kHz |
| ISOLATION | 500 V _{DC} | 100 | - | - | 100 | - | - | ΜΩ |
| MTBF (MIL-HDBK-217F) | GM @ T _C = 55°C | - | 468 | - | - | 468 | - | kHrs |
| DYNAMIC | | | | | | | | |
| Load Step Output Transient V _{OUT} | - Half Load to Full Load | - | 200 | 400 | - | 200 | 400 | mV_{PK} |
| Load Step Recovery ² | THAIL LUAU TU FUII LUAU | - | 200 | 400 | - | 200 | 400 | μSec |
| Line Step Output Transient ⁴ V _{OUT} | ep Output Transient ⁴ V _{OUT} | | 450 | 700 | - | 450 | 700 | mV_{PK} |
| Line Step Recovery ^{2, 4} $V_{IN} = 16V \text{ to } 40V$ | | - | 300 | 500 | - | 300 | 500 | μSec |
| Turn On Delay V _{OUT} | \/ = 0\/ to 20\/ | - | 10 | 20 | - | 10 | 20 | mSec |
| Turn On Overshoot | $V_{IN} = 0V \text{ to } 28V$ | - | 0 | 50 | - | 0 | Max | mV_{PK} |

Notes:

- 2. Time for output voltage to settle within 1% of its nominal value.
- Dependant on output voltage.
 Derate linearly to 0 at 110°C.
- 4. Verified by qualification testing.



BLOCK DIAGRAM

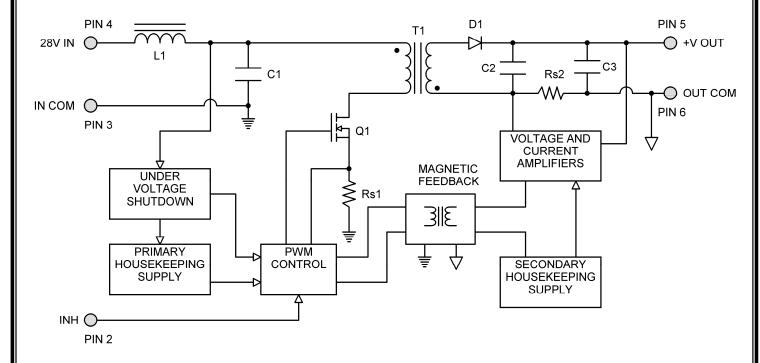


Figure 2

CONNECTION DIAGRAM

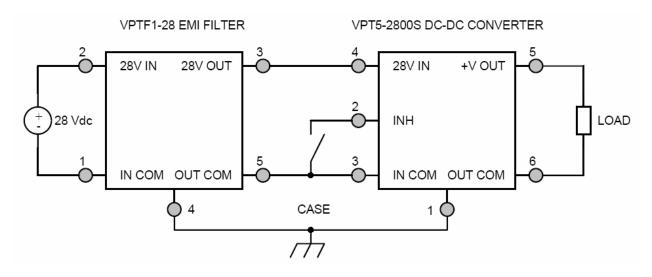


Figure 3 (Shown with optional EMI filter)



INHIBIT DRIVE CONNECTION DIAGRAMS

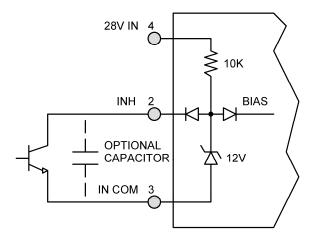


Figure 4 – Inhibit Circuit (Shown with optional capacitor for turn-on delay)



EFFICIENCY PERFORMANCE CURVES (T_{CASE} = 25°C, Full Load, Unless Otherwise Specified)

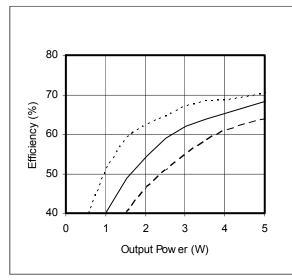


Figure 5 – VPT5-283R3S Efficiency (%) vs. Output Power (W)

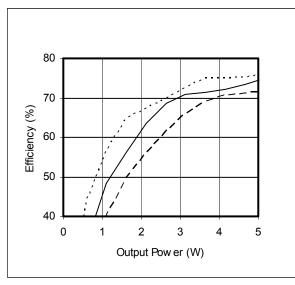


Figure 7 – VPT5-2812S Efficiency (%) vs. Output Power (W)

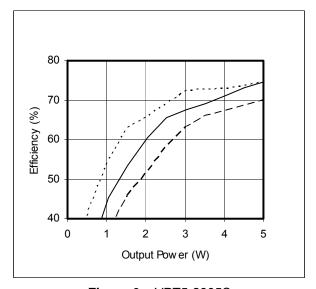


Figure 6 – VPT5-2805S Efficiency (%) vs. Output Power (W)

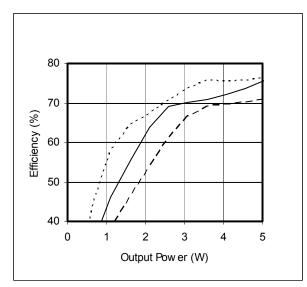


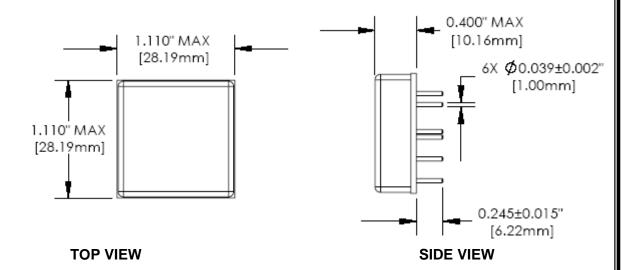
Figure 8 – VPT5-2815S
Efficiency (%) vs. Output Power (W)

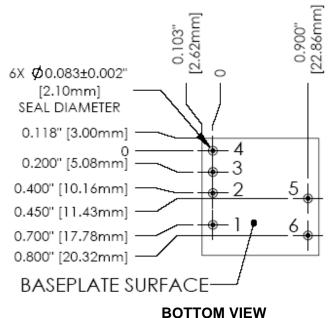


EMI PERFORMANCE CURVES (T_{CASE} = 25°C, V_{IN} = +28V ± 5%, Full Load, Unless Otherwise Specified) MIL-STD-461C CE03 MII -STD-461C CE03 Narrowband Emission Level [dBµA] Narrowband 80 80 60 60 40 40 20 20 0 0 0.01 0.01 Frequency [MHz] Frequency [MHz] MIL-STD-461C CE03 MIL-STD-461C CE03 Emission Level [dBµA] Narrowhand Emission Level [dBµA] Narrowband 40 40 OVERLOAD 30 30 20 20 after the state of 10 10 0 0 Frequency [MHz] Frequency [MHz] Figure 9 –VPT5-2800S without EMI Filter Figure 10 – VPT5-2800S with VPTF Series EMI Filter MIL-STD-461E CE102 MIL-STD-461E CE102 Emission Level [dBµV] Narrowband Emission Level [dBµV] Narrowband 90 90 70 70 50 50 30 30 10 10 0.01 0.01 0.1 Frequency [MHz] Frequency [MHz] MIL-STD-461E CE102 MIL-STD-461E CE102 Emission Level [dBµV] Emission Level [dBµV] Narrowband Narrowband 90 90 IF OVE ALOAD 70 70 50 50 30 30 10 10 0.1 Frequency [MHz] Frequency [MHz] Figure 12 -VPT5-2800S with VPTF Series EMI Filter Figure 11 -VPT5-2800S without EMI Filter



PACKAGE SPECIFICATIONS





| PIN | FUNCTION |
|-----|----------|
| 1 | CASE |
| 2 | INHIBIT |
| 3 | IN COM |
| 4 | 28V IN |
| 5 | +V OUT |
| 6 | OUT COM |

BOTTOW VIEW

Figure 13 – Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)

Package Notes:

- 1. Case temperature is measured on the center of the baseplate surface.
- 2. Materials: Baseplate aluminum, chromate conversion coating.

Cover – cold rolled steel, nickel plated.

Pins – copper, gold over nickel plating.



PACKAGE PIN DESCRIPTION

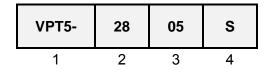
| Pin | Function | Description |
|-----|----------|--|
| 1 | CASE | Case Connection |
| 2 | INHIBIT | This is an open collector input. Logic Low = Disabled Output. Connect the inhibit pin to input common to disable the output. Unconnected, open collector or open drain = Enabled Output. |
| 3 | IN COM | Input Return Connection |
| 4 | 28V IN | Positive Input Voltage Connection |
| 5 | +V OUT | Positive Output Voltage Connection |
| 6 | OUT COM | Output Return Connection |

100% ENVIRONMENTAL SCREENING

| Screening | Condition |
|---------------------|---|
| Internal Visual | IPC-A-610 |
| Stabilization Bake | MIL-STD-883, Method 1008, Condition B, 125°C, 24 hours |
| Temperature Cycling | MIL-STD-883, Method 1010, Condition B, -55°C to +125°C, 10 Cycles |
| Burn-In | MIL-STD-883, Method 1015, 96 hours at +100°C |
| Final Electrical | 100% at 25°C |
| External Visual | MIL-STD-883, Method 2009 |



ORDERING INFORMATION



(1) (2) (3)

| Product Series | Nominal In | put Voltage | Output Voltage | | Number of Outputs | | |
|----------------|------------|-------------|-----------------------|--|-------------------|--------|--|
| VPT5- | 28 | 28 Volts | 3R3 05 12 15 | 3.3 Volts 5 Volts 12 Volts 15 Volts | S | Single | |

CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

Phone: (425) 353-3010 **Fax**: (425) 353-4030

E-mail: vptsales@vpt-inc.com

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